PERFORMANCE ANALYSIS FOR PILOT-BASED 1-BIT CHANNEL ESTIMATION WITH UNKNOWN QUANTIZATION THRESHOLD

Manuel Stein†, Shahar Bar*, Josef A. Nossek†, and Joseph Tabrikian*

†Institute for Circuit Theory and Signal Processing, Technische Universität München, Germany
*Department of Electrical and Computer Engineering, Ben-Gurion University of the Negev, Israel
E-mail: manuel.stein@tum.de, shahba@post.bgu.ac.il, josef.a.nossek@tum.de, joseph@ee.bgu.ac.il

ABSTRACT
Parameter estimation using quantized observations is of importance in many practical applications. Under a symmetric 1-bit setup, consisting of a zero-threshold hard-limiter, it is well known that the large sample performance loss for low signal-to-noise ratios (SNRs) is moderate ($\frac{\pi}{2}$ or $-1.96\text{dB}$). This makes low-complexity analog-to-digital converters (ADCs) with 1-bit resolution a promising solution for future wireless communications and signal processing devices. However, hardware imperfections and external effects introduce the quantizer with an unknown hard-limiting level different from zero. In this paper, the performance loss associated with pilot-based channel estimation, subject to an asymmetric hard limiter with unknown offset, is studied under two setups. The analysis is carried out via the Cramér-Rao lower bound (CRLB) and an expected CRLB for a setup with random parameter. Our findings show that the unknown threshold leads to an additional information loss, which vanishes for low SNR values or when the offset is close to zero.

Index Terms— Parameter estimation, nuisance parameter, 1-bit ADC, hard limiter, quantization offset

1. INTRODUCTION
Due to the fact that the complexity of a sampling device scales exponentially $O(2^b)$ with the number of bits $b$ which are used for the representation of the digital amplitude values, the analog-to-digital converter (ADC) has been identified as a crucial part for the design of hardware and energy-efficient signal processing systems [1]. Thus, from a hardware-aware perspective, ADCs with 1-bit output are an attractive design option. On the other hand, the highly nonlinear transformation associated with hard-limiting causes a substantial information loss during the transition from the analog to the digital domain. Fortunately, the performance loss for low SNR is $\frac{\pi}{2}$ or equivalently $-1.96\text{dB}$, and therefore moderate [2]. Also, using a higher sampling rate [3, 4] or adjustment of the analog radio front-end [5] allows compensation for the quantization loss. In addition, the optimization of the 1-bit ADC itself has been studied. The work of [6] shows how to set the hard-limiting threshold in order to maximize the Fisher information, while [7] focuses on communication rates and studies the Shannon information with 1-bit quantization and asymmetric known offsets at the receiver. Another line of works, e.g. [8, 9], investigates randomization of the quantization level, i.e., applying dithering before the quantizer.

A problem that arises when the quantization level of a 1-bit ADC is to be adaptively controlled, is the fact that a high resolution digital-to-analog converter (DAC) is required in order to set an analog offset voltage source. As also the complexity of DACs scales $O(2^b)$ with the number of bits $b$, this stands in contradiction with the main motivation behind 1-bit ADC technology, which is an energy and hardware efficient radio front-end. Therefore, low-complexity 1-bit ADCs will lack the feature of an accurately adjustable quantization level. Rather, a low-cost sampling device will be constructed such that the hard-limiting level is fixed to a constant value. Inevitable mismatches of the circuit parts during the production process and external effects will lead to an unknown quantization level of the sampler during runtime.

In this paper, the performance loss associated with 1-bit quantization and an unknown threshold is analyzed for the application of pilot-based channel estimation. The problem is first studied under the assumption that the channel parameter and quantization level are deterministic unknown. Then, a hybrid setup is considered, where the channel parameter is subject to a (known) prior distribution, while the hard-limiting offset is deterministic. The considered ADC allows to operate at high rates and provides binary data that can be stored on a small amount of memory. Thus, we focus on the asymptotic regime, where the number of samples $N$ is large. We characterize the optimal performance in the asymptotic regime, which is associated with the maximum likelihood estimator (MLE) for the deterministic setup and the joint maximum a-posteriori probability-maximum likelihood estimator (JMAP-ML) for the hybrid setup and establish the
1-bit loss with an unknown quantization level.

2. SYSTEM MODEL

2.1. Ideal Receiver ($\infty$-bit resolution)

For the analysis we consider the problem of pilot-based channel estimation. Therefore, the digital signal model of the ideal receive system with infinite resolution is given by

$$ y = \zeta x + \eta, $$

(1)

where $x \in \mathbb{R}^N$ is a pilot signal of known structure, $\zeta \in \mathbb{R}$ characterizes the channel attenuation, and $\eta \in \mathbb{R}^N$ is unit-variance white Gaussian noise. Therefore, given the parameter $\zeta$, the received signal $y \in \mathbb{R}^N$ follows the conditional probability law $y|\zeta \sim N(\zeta x, I)$, where $0 \in \mathbb{R}^N$ denotes the all-zeros vector and $I \in \mathbb{R}^{N \times N}$ is the identity matrix. We assume a quasi-ergodic phase-shift keying (BPSK) transmitter and a synchronized receiver such that $x \in \{-1, 1\}^N$ where $\frac{1}{N} \sum_{n=1}^{N} x_n = 0$.

2.2. Low-Complexity Receiver (1-bit resolution)

The low-complexity receiver under investigation is equipped with a 1-bit ADC which provides the digital signal

$$ r = \text{sign}(y - \alpha 1) = \text{sign}(\zeta x + \eta - \alpha 1), $$

(3)

where $\text{sign}(x)$ is the element-wise signum function

$$ [\text{sign}(x)]_n = \begin{cases} +1 & \text{if } x_n \geq 0 , \ n = 1, \ldots, N , \\ -1 & \text{if } x_n < 0 , \end{cases} $$

(4)

and $1 \in \mathbb{R}^N$ denotes the all-ones vector and $\alpha \in \mathbb{R}$ forms the unknown threshold level. The conditional probability density function (PDF) of the resulting binary received signal is

$$ p_r(r; \zeta, \alpha) = \prod_{n=1}^{N} Q(r_n(\alpha - \zeta x_n)), $$

(5)

with $Q(x)$ being the Q-function

$$ Q(x) = \frac{1}{\sqrt{2\pi}} \int_{x}^{\infty} \exp \left( -\frac{z^2}{2} \right) dz. $$

(6)

The task of the receivers is to calculate the estimates $\hat{\zeta}_y(y)$ and $\hat{\zeta}_r(r)$ by using the received signal $y$ or $r$, respectively.

2.3. Deterministic Approach

Under this approach the ideal receiver treats $\zeta$ as deterministic unknown, such that the asymptotically optimum unbiased estimator is the maximum likelihood estimator (MLE), given by

$$ \hat{\zeta}_y(y) \triangleq \arg \max_{\zeta \in \mathbb{R}} p_y(y; \zeta) $$

$$ = \arg \max_{\zeta \in \mathbb{R}} \sum_{n=1}^{N} \ln p_{y_n}(y_n; \zeta), $$

(7)

with the corresponding error

$$ \text{MSE}_y(\zeta) \triangleq \mathbb{E}_{y,\zeta} \left[ (\hat{\zeta}_y(y) - \zeta)^2 \right]. $$

(8)

The 1-bit receiver considers both $\zeta$ and the threshold $\alpha$ as deterministic unknown. The MLE is based on joint estimation of both the parameter $\zeta$ and the threshold $\alpha$, such that

$$ \begin{bmatrix} \hat{\zeta}_r(r) \\ \hat{\alpha}_r(r) \end{bmatrix} \triangleq \arg \max_{\zeta, \alpha \in \mathbb{R}} p_r(r; \zeta, \alpha) $$

$$ = \arg \max_{\zeta, \alpha \in \mathbb{R}} \sum_{n=1}^{N} \ln p_{r_n}(r_n; \zeta, \alpha), $$

(9)

with the corresponding error

$$ \text{MSE}_r(\zeta, \alpha) \triangleq \mathbb{E}_{r,\zeta,\alpha} \left[ (\hat{\zeta}_r(r) - \zeta)^2 \right]. $$

(10)

2.4. Hybrid Approach

The second approach considers the parameter $\zeta$ to be modeled as a random variable with a prior PDF $p_\zeta(\zeta)$. The received signal $y$ and the parameter of interest $\zeta$ follow the joint PDF $p_{y,\zeta}(y, \zeta)$. The asymptotically optimum estimator is the maximum a-posteriori probability (MAP) estimator

$$ \hat{\zeta}_y(y) \triangleq \arg \max_{\zeta \in \mathbb{R}} p_{y|\zeta}(y| \zeta) $$

$$ = \arg \max_{\zeta \in \mathbb{R}} (\ln p_{y|\zeta}(y| \zeta) + \ln p_\zeta(\zeta)), $$

(11)

where the last equality stems from Bayes law. The corresponding error is defined as

$$ \text{MSE}_y \triangleq \mathbb{E}_{y,\zeta} \left[ (\hat{\zeta}_y(y) - \zeta)^2 \right]. $$

(12)

The 1-bit receiver treats $\zeta$ as random while the threshold $\alpha$ remains deterministic unknown. The received signal $r$ and the parameter $\zeta$ follow the joint PDF $p_{r,\zeta}(r, \zeta; \alpha)$. For the 1-bit receiver, the asymptotically optimum estimator [11] in the MSE sense is the JMAP-MLE [12], given by

$$ \begin{bmatrix} \hat{\zeta}_r(r) \\ \hat{\alpha}_r(r) \end{bmatrix} \triangleq \arg \max_{\zeta, \alpha \in \mathbb{R}} p_{r,\zeta}(r; \zeta; \alpha) $$

$$ = \arg \max_{\zeta, \alpha \in \mathbb{R}} (\ln p_{r|\zeta}(r| \zeta; \alpha) + \ln p_\zeta(\zeta)), $$

(13)
The corresponding error is defined as
\[ \text{MSE}_r(\alpha) \triangleq \text{Er},\zeta;\alpha \left[ (\hat{\zeta}_r(r) - \zeta)^2 \right]. \]  
(14)

## 3. PERFORMANCE ANALYSIS

In this section, the expressions for the MSEs in (8), (10), (12), and (14) are evaluated. Note that due to the possibility of high sampling rates with 1-bit ADC, the focus is on the asymptotic regime, where the number of samples \( N \) is large. Thus, the CRLB and its expected version utilized in the sequel are used as valid approximations to the subjected MSEs.

### 3.1. Deterministic Approach - Hard-limiting Loss

With the ideal receiver and estimation by the MLE, the MSE can be approximated asymptotically by the CRLB [13, 14]
\[ \text{MSE}_y(\xi) \triangleq F_y^{-1}(\xi), \]
(15)
where with (1) the Fisher information (FI) [15] is
\[ F_y(\xi) = \text{E}_{y,\zeta} \left[ \left( \frac{\partial \text{ln} p_y(y; \xi)}{\partial \xi} \right)^2 \right] = \sum_{n=1}^{N} x_n^2 = N. \]
(16)

For the 1-bit receiver, the estimation of the threshold \( \hat{a}_r(r) \) has an effect onto the inference of the attenuation parameter \( \zeta_r(r) \). The corresponding CRLB for the estimator \( \hat{\zeta}_r(r) \) is
\[ \text{MSE}_r(\xi, \alpha) \triangleq \frac{F_{r,\alpha}(\xi, \alpha)}{F_{r,\xi}(\xi, \alpha) F_{\alpha,\alpha}(\xi, \alpha) - F_{r,\alpha}(\xi, \alpha)}, \]
(17)
The required FIs are given by
\[ F_{r,\xi}(\xi, \alpha) = \text{E}_{r,\xi,\alpha} \left[ \left( \frac{\partial \text{ln} p_r(r; \xi, \alpha)}{\partial \xi} \right)^2 \right] = \sum_{n=1}^{N} \text{E}_{r,\xi,\alpha} \left[ x_n^2 \exp \left( -(\alpha - \zeta x_n)^2 \right) \right] = \sum_{n=1}^{N} 2\pi Q(\alpha - \zeta x_n)^2 \]
\[ = N \left( \phi_+ (\xi, \alpha) + \phi_- (\xi, \alpha) \right), \]
(18)
where the third equality stems from (5), such that
\[ \text{E}_{r,\xi,\alpha} \left[ \frac{1}{Q^2(r_n(\alpha - \zeta x_n))} \right] = \sum_{r_n = \pm 1} \frac{Q(r_n(\alpha - \zeta x_n))}{Q^2(r_n(\alpha - \zeta x_n))} = \frac{1}{Q(\alpha - \zeta x_n) - Q^2(\alpha - \zeta x_n)}, \]
(19)
and the last equality stems from the BPSK modulation of \( \{ x_n \}_{n=1}^{N} \) and the definition
\[ \phi_\pm (\xi, \alpha) = \frac{\exp \left( -(\alpha \pm \xi)^2 \right)}{2\pi Q(\alpha \pm \xi) - Q^2(\alpha \pm \xi)}. \]
(20)

In the same manner,
\[ F_{r,\alpha}(\xi, \alpha) = \text{E}_{r,\xi,\alpha} \left[ \left( \frac{\partial \text{ln} p_r(r; \xi, \alpha)}{\partial \alpha} \right)^2 \right] = \sum_{n=1}^{N} \text{E}_{r,\xi,\alpha} \left[ \frac{\exp \left( -(\alpha - \zeta x_n)^2 \right)}{2\pi Q^2(r_n(\alpha - \zeta x_n))} \right] = \frac{N}{2} \left( \phi_+ (\xi, \alpha) + \phi_- (\xi, \alpha) \right), \]
(21)
Note that if the quantization level is known to the 1-bit receiver, the performance is limited to
\[ \text{MSE}_r(\xi, \alpha) \triangleq F_{r,\alpha}(\xi, \alpha). \]
(23)

To characterize the information loss introduced by the hard-limiter, we define the quantization loss via two MSE ratios
\[ \chi(\xi, \alpha) \triangleq \frac{\text{MSE}_y(\xi)}{\text{MSE}_r(\xi, \alpha)} = \frac{\text{F}_{r,\alpha}(\xi, \alpha) F_{r,\alpha}(\xi, \alpha) - \text{F}_{r,\alpha}(\xi, \alpha)}{\text{F}_{r,\alpha}(\xi, \alpha) \text{F}_{y,\alpha}(\xi, \alpha)} \]
\[ = 2 \left( \phi_+ (\xi, \alpha) + \phi_- (\xi, \alpha) \right), \]
(24)
which are associated with the ideal receiver and with a known quantization level \( \alpha \) at the 1-bit receiver, respectively. Fig. 1 shows the performance loss (24) for different SNR levels in solid lines, where we use the convention \( \text{SNR} = \zeta^2 \). Note that the loss is symmetric for negative \( \alpha \) or \( \zeta \). The results show that for the considered application a quantization level \( \alpha \) close to zero is in general preferable and that the performance gap increases with the SNR. Additionally, with dashed lines, Fig. 1 shows the alternative loss (25). While in the low SNR regime the estimation of \( \alpha \) has no effect onto the estimation of \( \zeta \), the situation changes within the medium SNR regime. Here the fact that the threshold is unknown can have a significant effect, in particular when \( \alpha \) is not close to zero.

### 3.2. Hybrid Approach - Hard-limiting Loss

In the case of a random channel parameter, with the ideal receiver, the asymptotic performance of the MAP estimator can
be characterized using the expected CRLB [18, p. 6]
\[
\text{MSE}_y \triangleq E_\zeta \left[ F_y^{-1}(\zeta) \right] = \frac{1}{N},
\]
where the last equality stems from taking the expectation of (15) with respect to \( \zeta \). For the 1-bit receiver, according to [16], as the number of measurements increases, the JMAP-MLE in (13), coincides (in the sense of convergence in probability) with the MLE, given by
\[
\begin{bmatrix} \hat{H}_r(\alpha) \\ \hat{\phi}_r(\alpha) \end{bmatrix} = \arg \max_{\zeta, \alpha} \ln p_y(r|\zeta; \alpha). \tag{27}
\]
The sequence of MLEs as a function of the number of measurements is asymptotically uniformly integrable [17]. Thus,
\[
\lim_{N \to \infty} \text{MSE}_r(\alpha) = \lim_{N \to \infty} E_{r, \zeta, \alpha} \left[ (\hat{H}_r(\alpha) - \zeta)^2 \right]
= \lim_{N \to \infty} E_\zeta \left\{ E_{r|\zeta, \alpha} \left[ (\hat{H}_r(\alpha) - \zeta)^2 \right] \right\}
= \lim_{N \to \infty} E_\zeta \left[ \frac{F_{r, \zeta, \alpha}(\zeta, \alpha)}{F_{r, \alpha, \alpha}(\zeta, \alpha)} - F_{r, \zeta, \alpha}^2(\zeta, \alpha) \right], \tag{28}
\]
where the last equality stems from taking the expectation of (17). Hence, by using (18), (21), and (22), one obtains
\[
\text{MSE}_r(\alpha) \triangleq E_\zeta \left[ \frac{F_{r, \alpha, \alpha}(\zeta, \alpha)}{F_{r, \zeta, \alpha}(\zeta, \alpha)F_{r, \alpha, \alpha}(\zeta, \alpha) - F_{r, \zeta, \alpha}^2(\zeta, \alpha)} \right]
= E_\zeta \left[ \frac{2}{N} \left( \phi_+^{(2)}(\zeta, \alpha) + \phi_-(\zeta, \alpha) \right) \right]
= \frac{1}{2N} \left( E_\zeta \left[ \frac{1}{\phi_-(\zeta, \alpha)} \right] + E_\zeta \left[ \frac{1}{\phi_+(\zeta, \alpha)} \right] \right) = \frac{1}{N} \Psi_H.
\]
where due to symmetry considerations, we define
\[
\Psi_H \triangleq E_\zeta \left[ \frac{1}{\phi_-(\zeta, \alpha)} \right] = E_\zeta \left[ \frac{1}{\phi_+(\zeta, \alpha)} \right]. \tag{30}
\]
The quantization losses are given by
\[
\begin{align*}
\chi(\alpha) &\triangleq \frac{\text{MSE}_y}{\text{MSE}_r(\alpha)} \equiv \frac{\Psi_H^{-1}}{\text{MSE}_r(\alpha)} \equiv \frac{\text{MSE}_y}{\text{MSE}_r(\alpha)} \equiv \frac{\Psi_H^{-1}}{\text{MSE}_r(\alpha)} \equiv \frac{1}{2 E_\zeta \left[ \frac{1}{\phi_+(\zeta, \alpha) + \phi_-(\zeta, \alpha)} \right]}.
\end{align*}
\]
Fig. 2 shows the performance loss (31) with solid lines. In this scenario SNR = \( \sigma_n^2 \) as the noise \( \eta_n \sim \mathcal{N}(0, 1) \). It can be observed that the quantization loss increases sharply when the hard-limiting level \( \alpha \) deviates from zero. Additionally, with dashed lines, Fig. 2 shows the alternative performance loss (32). Like in the deterministic setup, the unknown quantization introduces an additional loss which becomes small for low SNR and when the unknown threshold is close to zero.

4. CONCLUSION

We have analyzed the performance of a 1-bit receiver with respect to the task of channel parameter estimation when the quantization offset is unknown. In this situation the receiver has to estimate the quantization level which in general has an influence on the quality of the estimation of the channel parameter. In the low SNR regime and when the quantization offset is close to zero this effect vanishes. This confirms that 1-bit ADCs are an interesting option for low SNR applications while for the medium SNR regime signal processing with 1-bit ADCs requires careful hardware design.
5. REFERENCES


